

## EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	113	printed adj circuit adj board with motherboard with daughterboard	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2006/03/31 13:54
L2	2	printed adj circuit adj board with processor and processor with port and processor with development with port and (multiple plural\$4) with boards and (multiple plural\$4) with bus and processor with board and bus with processor and printed adj circuit adj board and (memory stor\$4) same board and board with (link\$4 communicat\$4 connect\$4 path) and dram and serial with bus	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2006/03/31 13:55
L3	5	printed adj circuit adj board with processor and processor with port and processor with development with port and (multiple plural\$4) with boards and (multiple plural\$4) with bus and processor with board and bus with processor and printed adj circuit adj board	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2006/03/31 13:55
L4	0	nick near henninger.in.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2006/03/31 13:55
L5	0	nick near henniger.in.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2006/03/31 13:56
L6	5	mick near henniger.in.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2006/03/31 13:56
L7	4	kelvin near liu.in.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2006/03/31 13:56
L8	48	ming near2 chi near2 chen.in.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2006/03/31 13:57
L9	26	ramesh near srinivasan.in.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2006/03/31 13:57

## EAST Search History

L10	0	severin near baer.in.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2006/03/31 13:58
L11	1	sanjoy near dey.in.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2006/03/31 13:58
L12	0	smitta near kiran near2 rane.in.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2006/03/31 13:58
L13	0	smitta near2 kiran near2 rane.in.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2006/03/31 13:58
L14	82	6 7 8 9 10 11 12	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2006/03/31 14:11
L15	5	(6 7 8 9 10 11 12) and printed near2 circuit near2 board	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2006/03/31 14:12
L16	3	(6 7 8 9 10 11 12) and (printed near2 circuit near2 board).clm.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2006/03/31 14:13
L17	0	(6 7 8 9 10 11 12) and ((printed near2 circuit near2 board) and memory near controller).clm.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2006/03/31 14:12
L18	0	(6 7 8 9 10 11 12) and ((printed near2 circuit near2 board) and development near port).clm.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2006/03/31 14:13
L19	0	(6 7 8 9 10 11 12) and ((printed near2 circuit near2 board) and (random near access near memory)).clm.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2006/03/31 14:13
L20	0	(6 7 8 9 10 11 12) and ((printed near2 circuit near2 board) and (operating near system)).clm.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2006/03/31 14:15
L21	0	(6 7 8 9 10 11 12) and ((printed near2 circuit near2 board) and (system near bus)).clm.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2006/03/31 14:15

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L22	0	(6 7 8 9 10 11 12) and ((printed near2 circuit near2 board) and (system )). clm.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2006/03/31 14:16
L23	0	(6 7 8 9 10 11 12) and ((printed near2 circuit near2 board) and (memory)). clm.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2006/03/31 14:16
L24	2	(6 7 8 9 10 11 12) and ((printed near2 circuit near2 board) and (mount\$4)). clm.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2006/03/31 14:16
L25	0	(6 7 8 9 10 11 12) and ((printed near2 circuit near2 board) and (mount\$4) and random).clm.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2006/03/31 14:16
L26	0	(6 7 8 9 10 11 12) and ((printed near2 circuit near2 board) and (mount\$4) and ram).clm.	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2006/03/31 14:16
L27	1	(boot\$up boot\$4) and mother\$board and emulat\$4 and port and port with emulat\$4 and (send\$4 transmit\$4 receiv\$4) with emulat\$4 and emulat\$4 with device and development with port	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2006/03/31 14:17
L28	1	(boot\$up boot\$4) and mother\$board and emulat\$4 and port and port with emulat\$4 and (send\$4 transmit\$4 receiv\$4) with emulat\$4 and emulat\$4 with device and development with port and memory near control\$4	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2006/03/31 14:18
L29	1	(boot\$up boot\$4) and mother\$board and emulat\$4 and port and port with emulat\$4 and (send\$4 transmit\$4 receiv\$4) with emulat\$4 and emulat\$4 with device and development with port and (memory near control\$4.clm. )	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2006/03/31 14:18
L30	0	(boot\$up boot\$4) and mother\$board and emulat\$4 and port and port with emulat\$4 and (send\$4 transmit\$4 receiv\$4) with emulat\$4 and emulat\$4 with device and development with port and (random near3 access\$4 near3 memory.clm.)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2006/03/31 14:19

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L31	0	(boot\$up boot\$4) and mother\$board and emulat\$4 and port and port with emulat\$4 and (send\$4 transmit\$4 receiv\$4) with emulat\$4 and emulat\$4 with device and development with port and (system near bus.clm.)	US-PGPUB; USPAT; EPO; JPO; IBM_TDB	OR	ON	2006/03/31 14:19
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